

REMARKS

In response to the Restriction Requirement mailed October 1, 2002, for the above-referenced application, Applicant hereby elects Group I as specified by the Examiner (claims 1-8, drawn to a method of making a high-frequency power transistor circuit, classified in class 438, subclass 106+). Non-elected claims 9-16 have been cancelled.

Respectfully submitted,

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